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Application Number:	10620456			
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Title of Invention:	MEMORY MODELING CIRCUIT WITH FAULT TOLERATION			
First Named Inventor/Applicant Name:	Chin Lee			
Customer Number:	2292			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)	
1 Issue Fee Payment (PTO-85B)	2007-11-21-IssueFee_Trans. pdf	64746	no	1		
		f424ccb4f7a0b09f23182620a5c53fbda a79d5af				
Warnings:						
Information:						
2 Fee Worksheet (PTO-06)	fee-info.pdf	8462	no	2		
		793606b14d657759aaf509ea3896ea85 a2b255af				
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